

## 3. USER MAINTENANCE ROUTINES

Maintenance routines performable by the OS-9000SRS operator are listed in this section. More advanced routines (i.e., procedures involving repairs or adjustments within the instrument) should be referred to Gold Star service personnel.

### 3-1. CLEANING

If the outside of the case becomes dirty or stained, carefully wipe the soiled surface with a rag moistened with detergent, then wipe the cleaned surface with a dry cloth. In case of severe stain, try a rag moistened with alcohol. Do not use powerful hydrocarbons such as benzene or paint thinner.

Dust and /or smudges can be removed from the CRT screen. First remove the front case and filter (see Figure 3-1). Clean the filter (and the CRT face, if necessary) by wiping carefully with a soft cloth or commercial wiping tissue moistened with a mild cleaning agent. Take care not to scratch them. Do not use abrasive cleanser or strong solvents. Let the cleaned parts air dry thoroughly before reinstalling the filter and front case. If installed wet, water rings may form and blur the waveforms. Be particularly careful not to get fingerprints on the filter or CRT face.

### 3-2. CALIBRATION INTERVAL

To maintain the accuracy specifications of the OS-9000SRS, calibration checks and procedures should be performed after every 1000 hours of service. However, if the instrument is used infrequently, the calibration checks should be performed every six months.

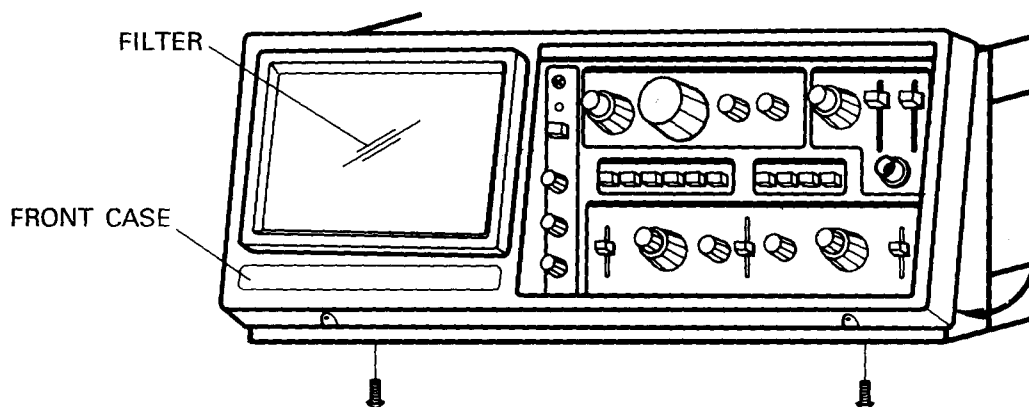
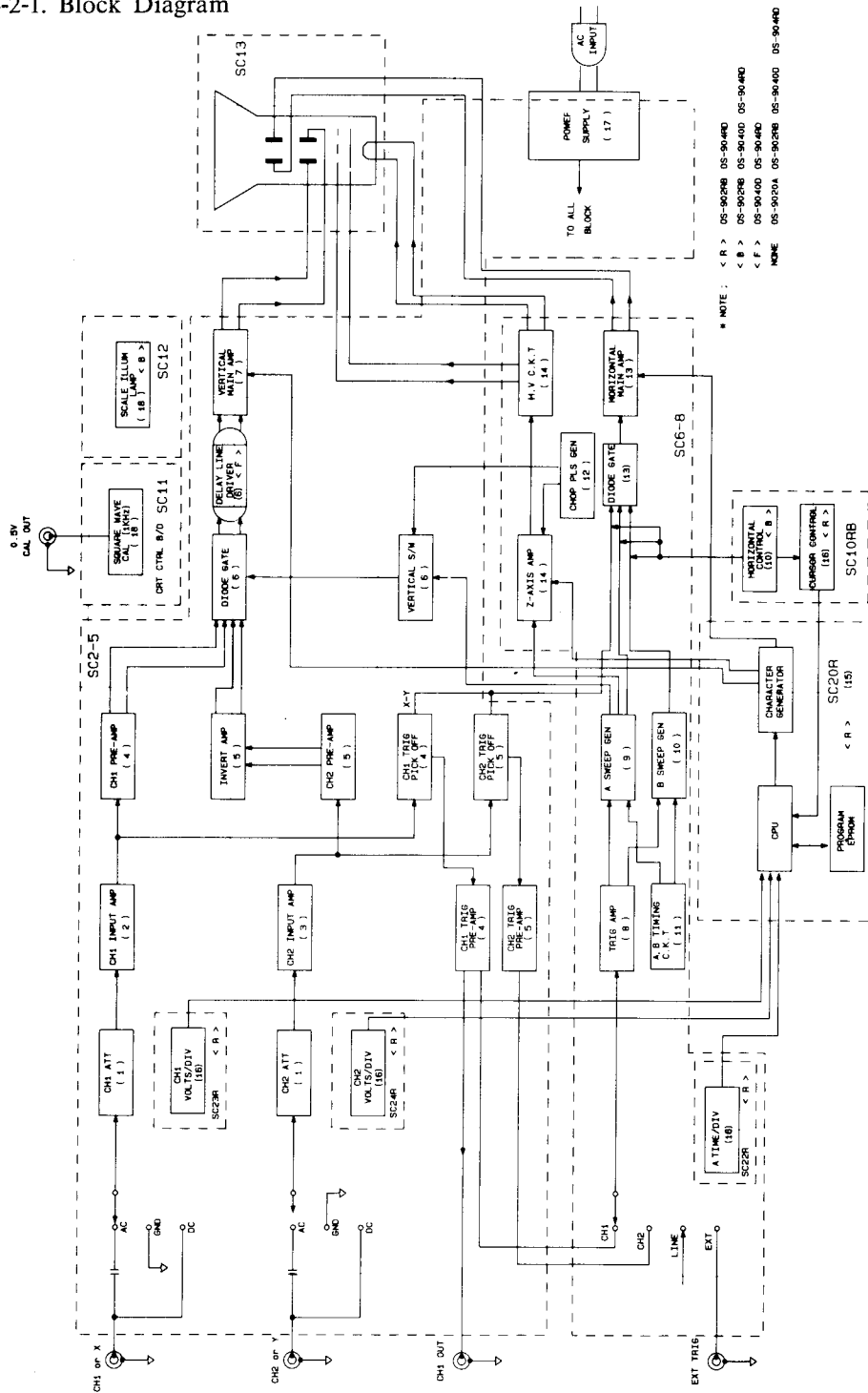


FIGURE 3-1. FRONT CASE AND FILTER

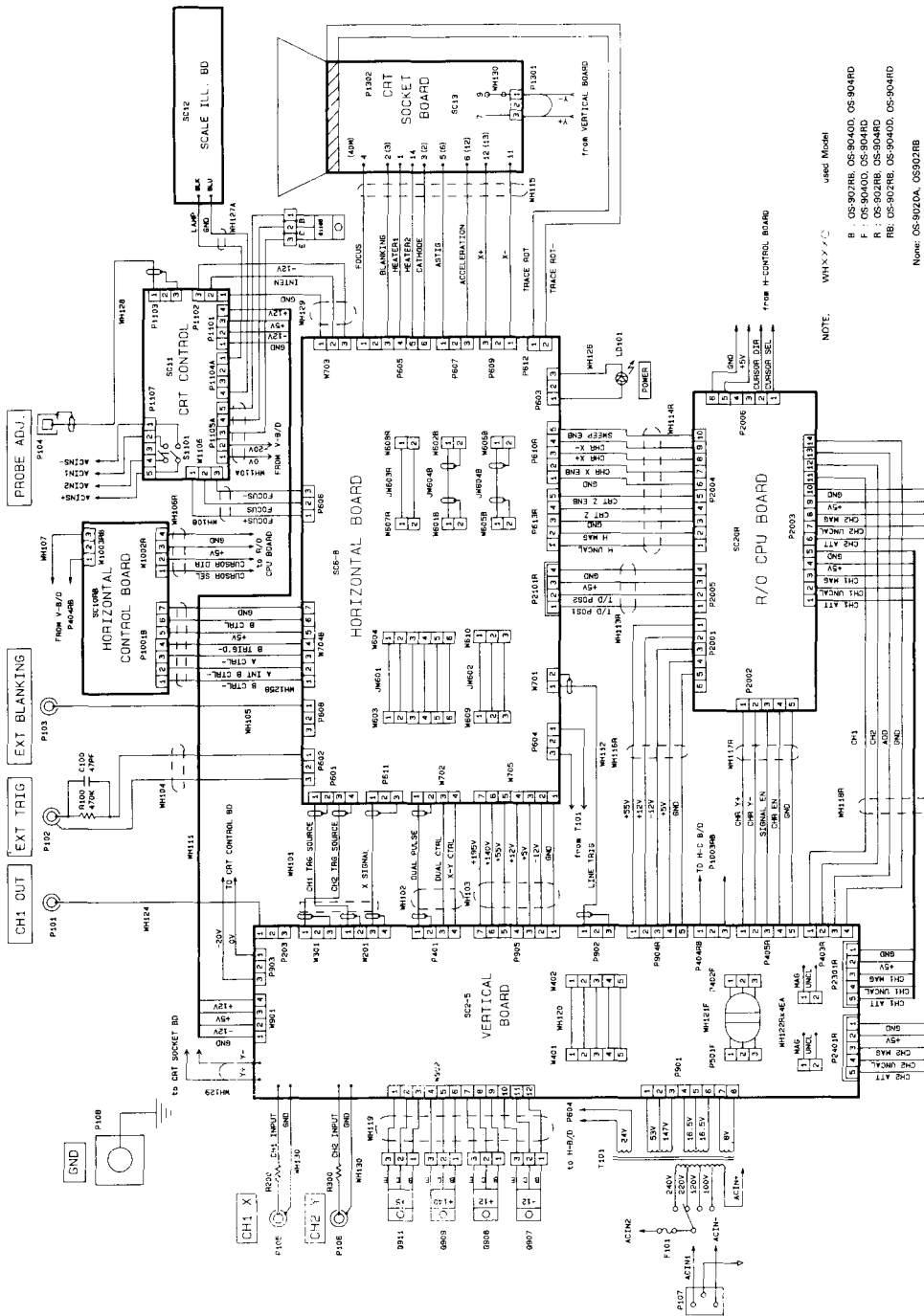


## 4-2. SCHEMATIC DIAGRAMS

### 4-2-1. Block Diagram

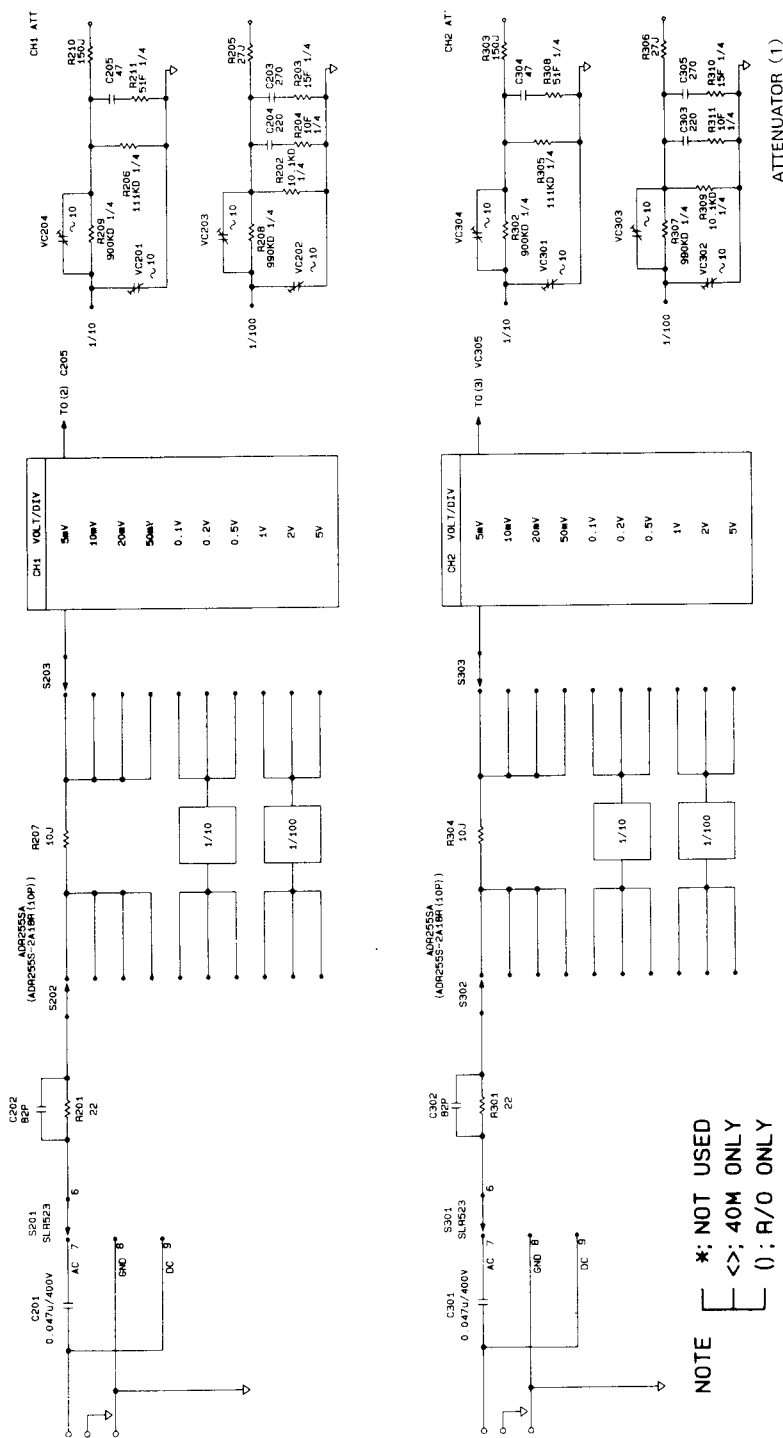


4-2-2. Wiring Diagram

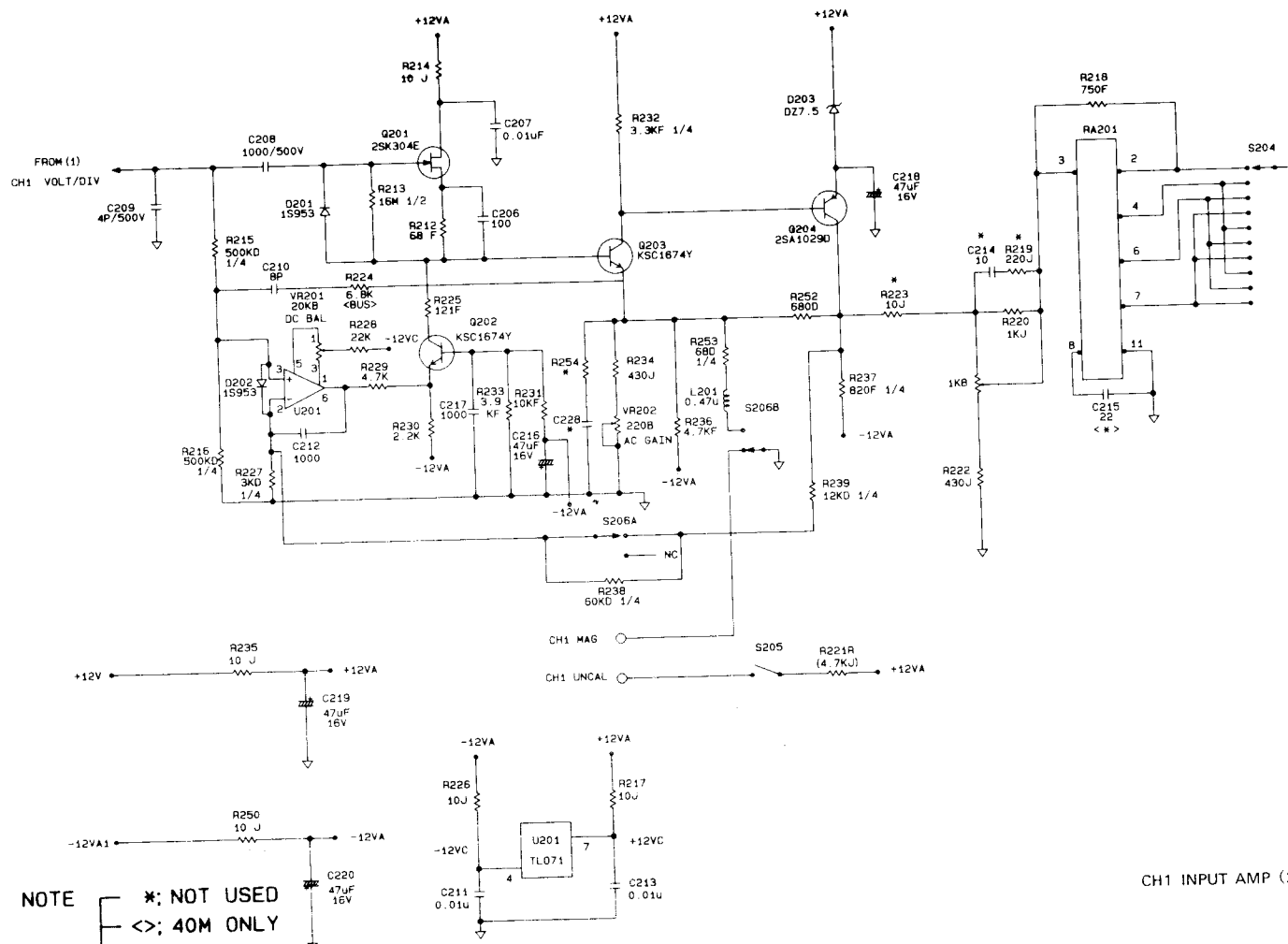


NOTE: VHX770 used Model  
 P OS-9027RB, OS-9040D, OS-9044D  
 R OS-9040D, OS-9044D  
 R OS-9040D, OS-9044D  
 RB OS-9027RB, OS-9040D, OS-9044D  
 Note: OS-9027RA, OS-9027RB  
 OS-9040D, OS-9044D

4-2-3. Circuit Diagrams

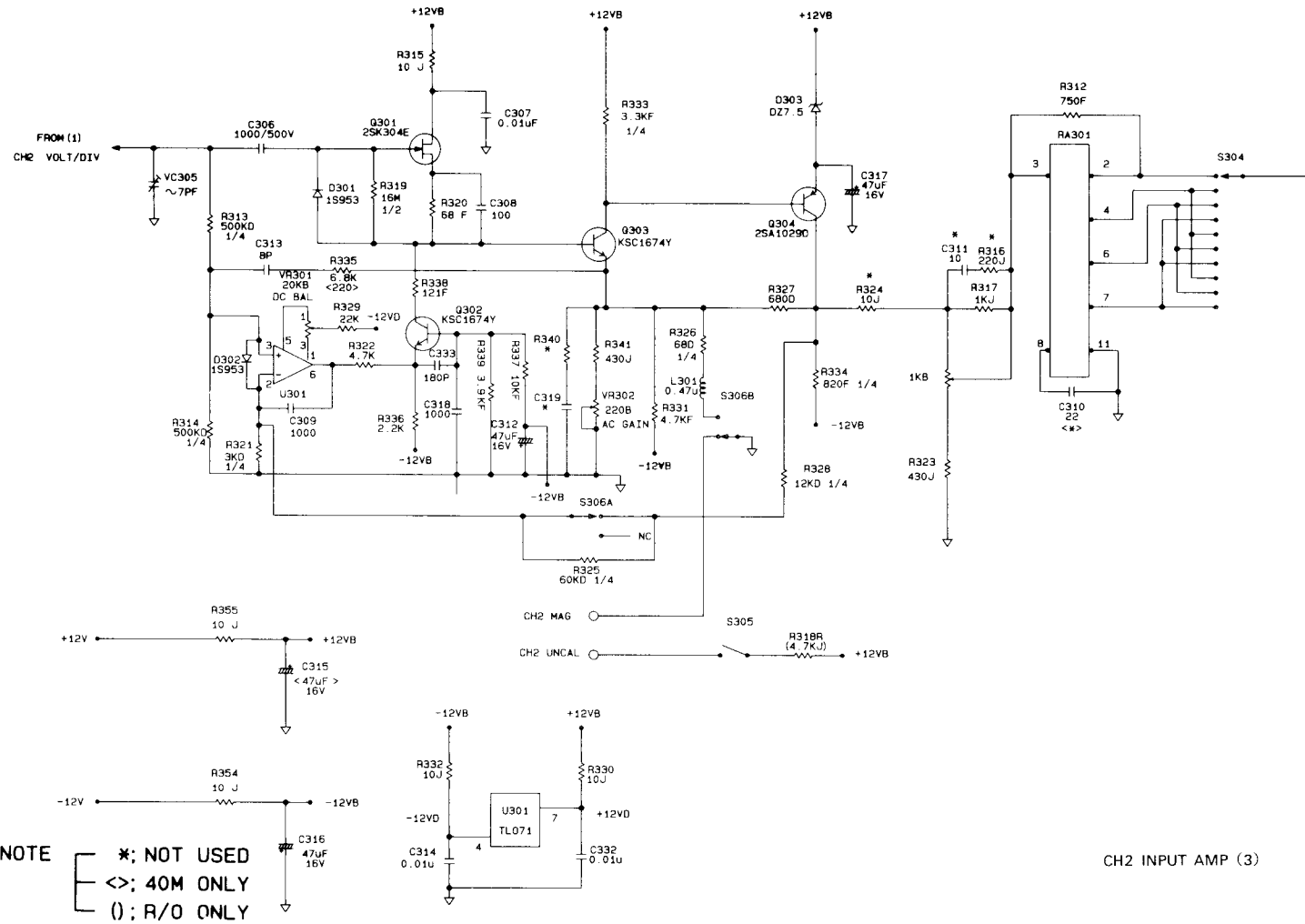


NOTE [ \*; NOT USED  
 [ <; 40M ONLY  
 [ 0; R/O ONLY

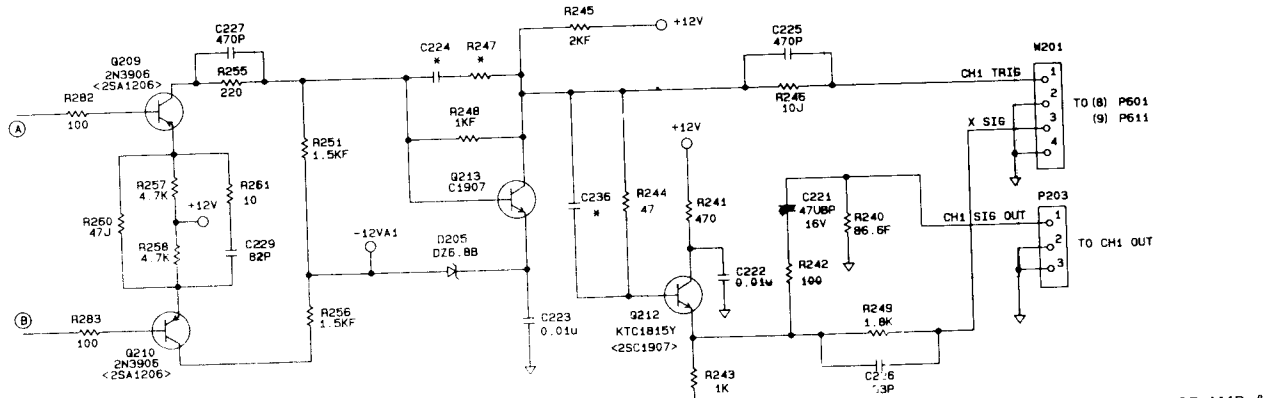
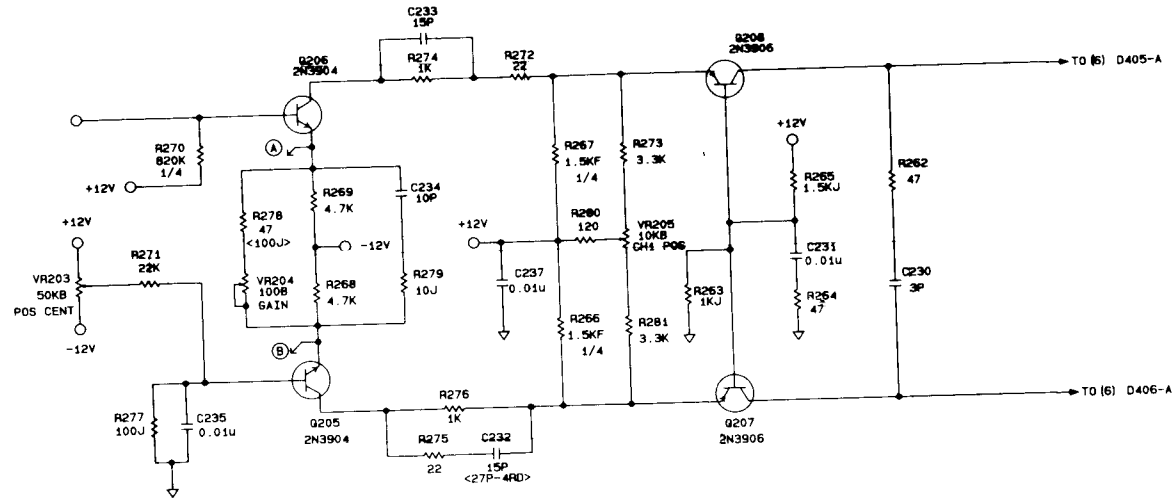


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CH1 INPUT AMP (2)



<>: 40M ONLY  
 (); R/O ONLY

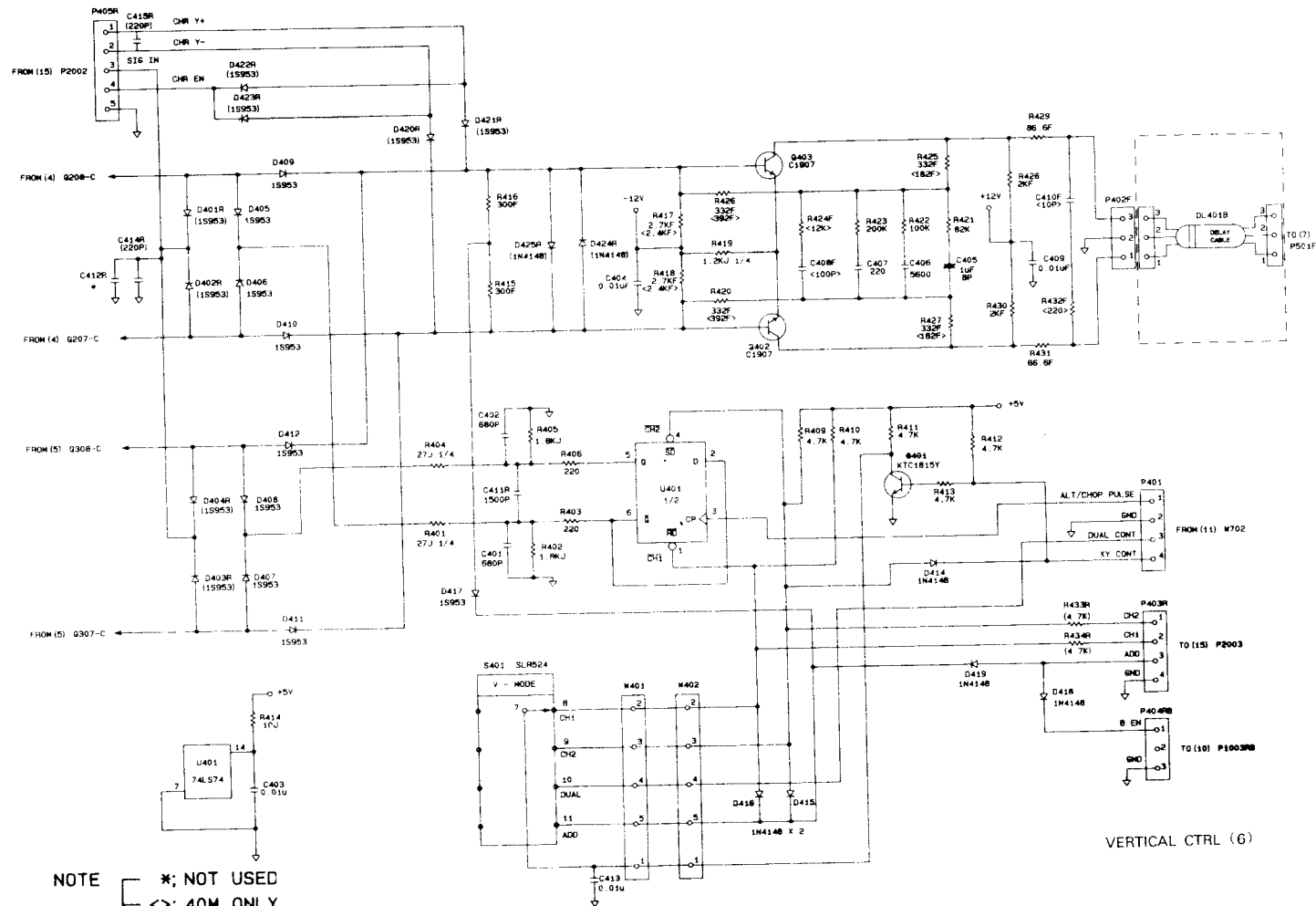


NOTE \*; NOT USED  
 <>; 40M ONLY  
 (); R/O ONLY

CH1 PRE AMP & TRIG PICK OFF (4)

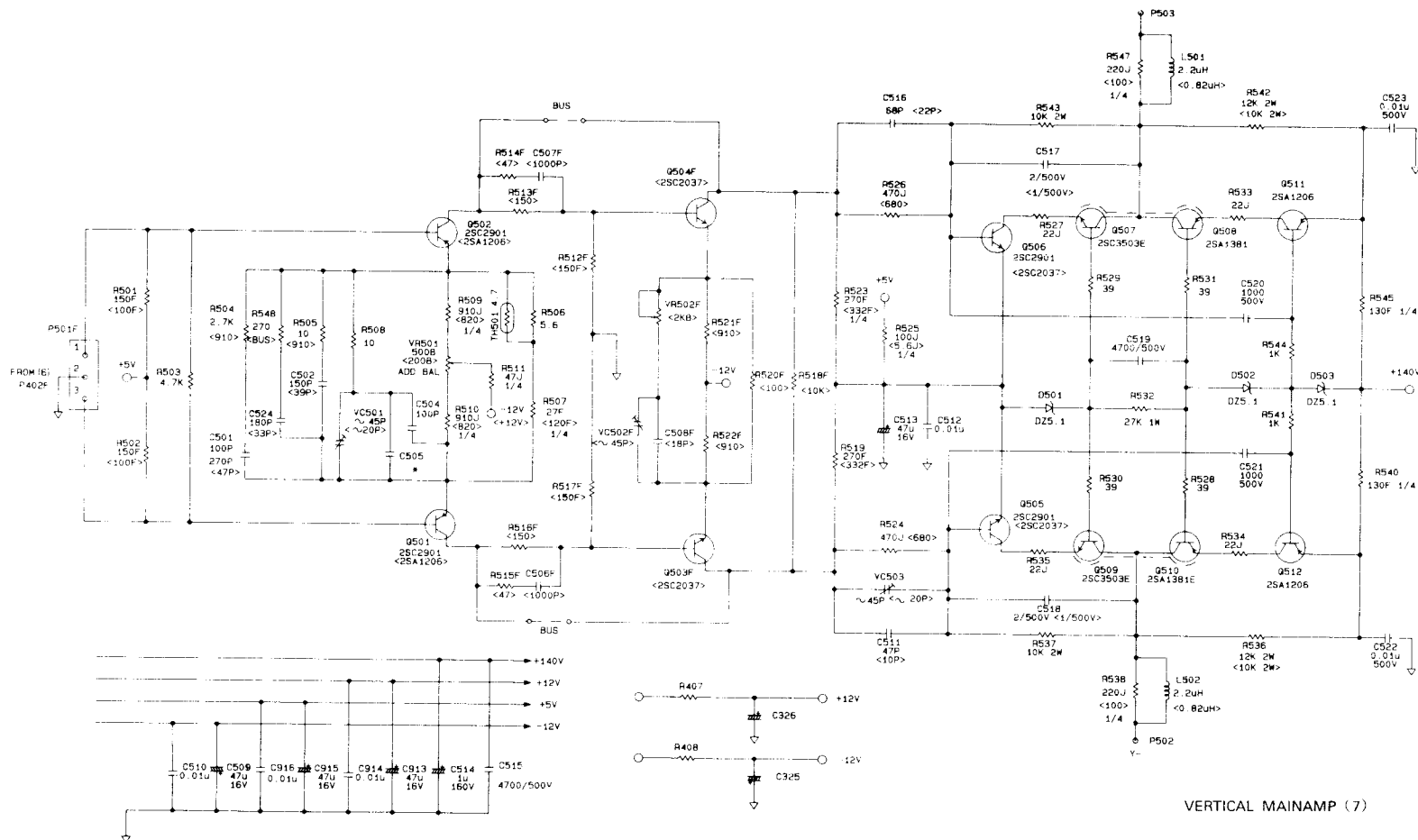






NOTE

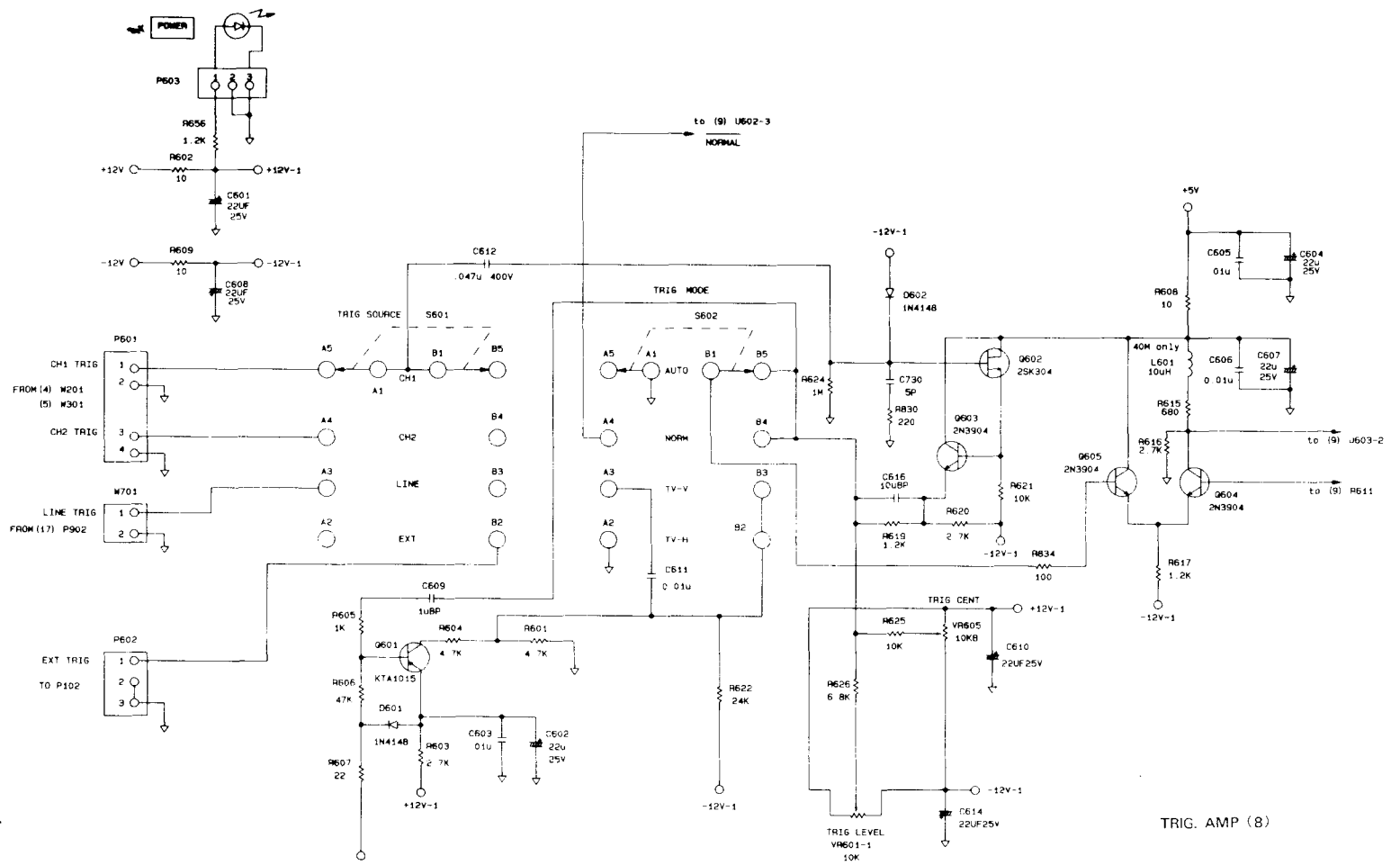
- \*; NOT USED
- <>; 40M ONLY
- () ; R/O ONLY

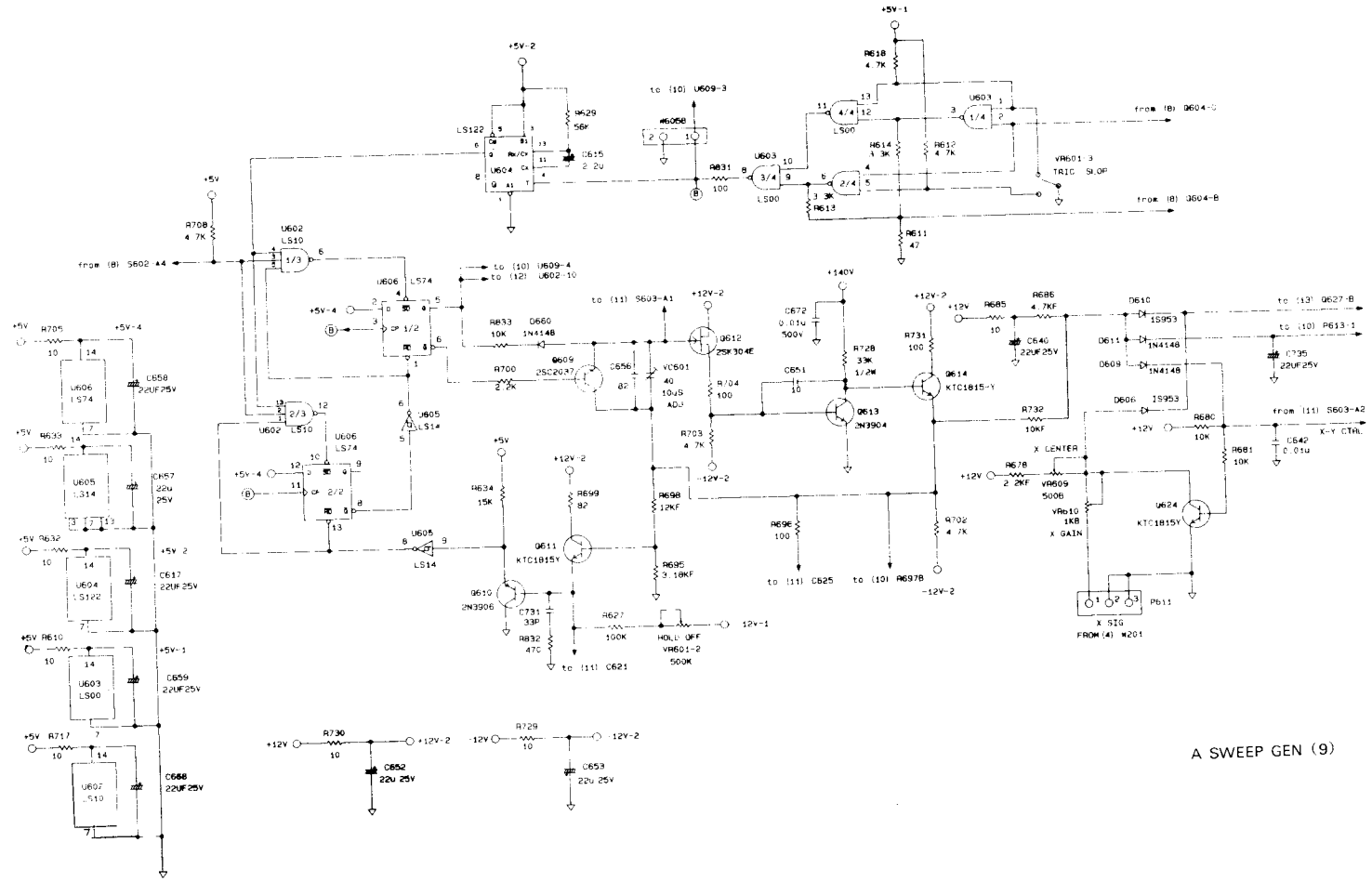


VERTICAL MAINAMP (7)

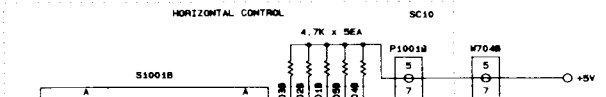
NOTE

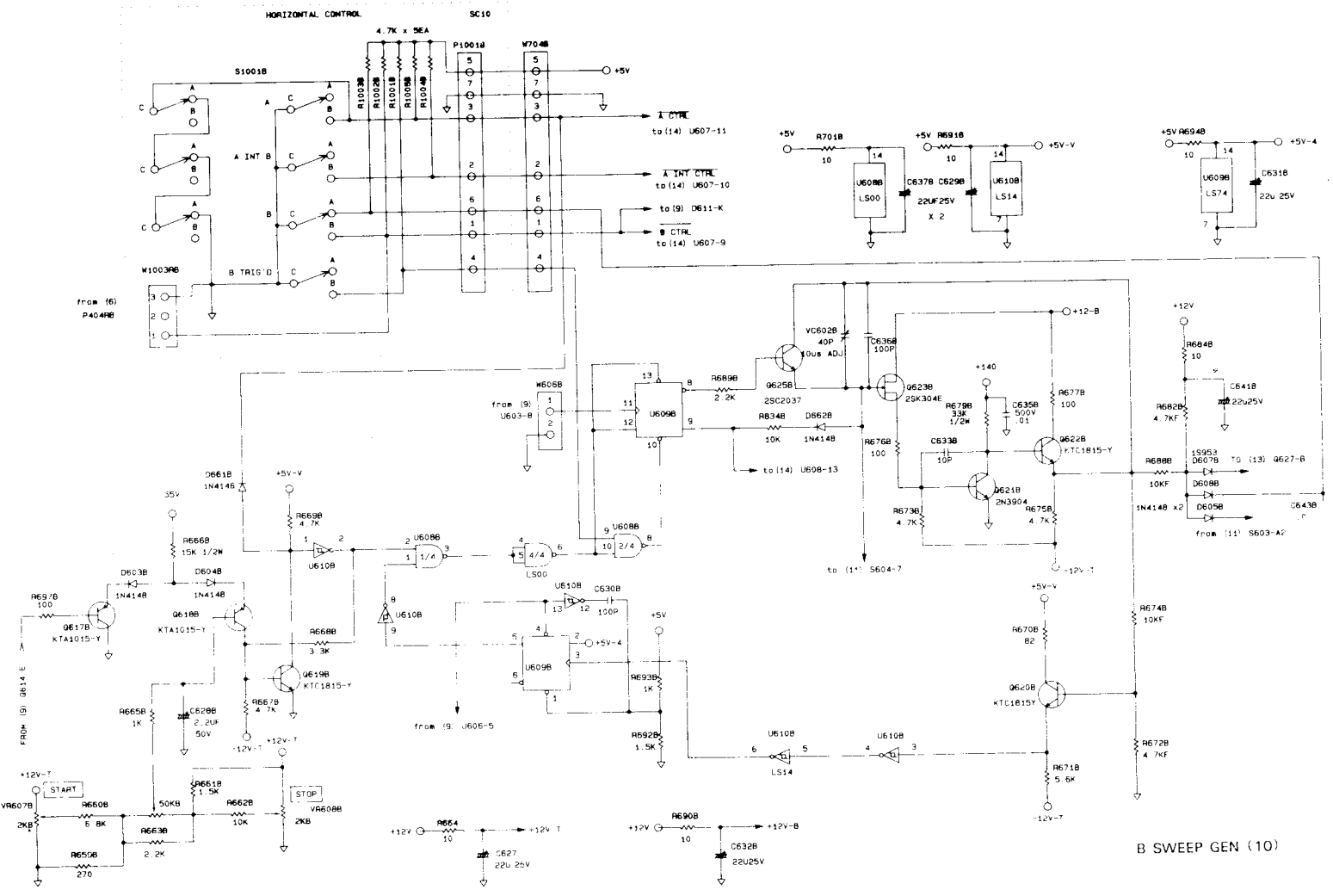
- \*: NOT USED
- <>: 40M ONLY
- (): R/O ONLY

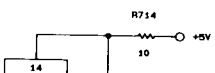
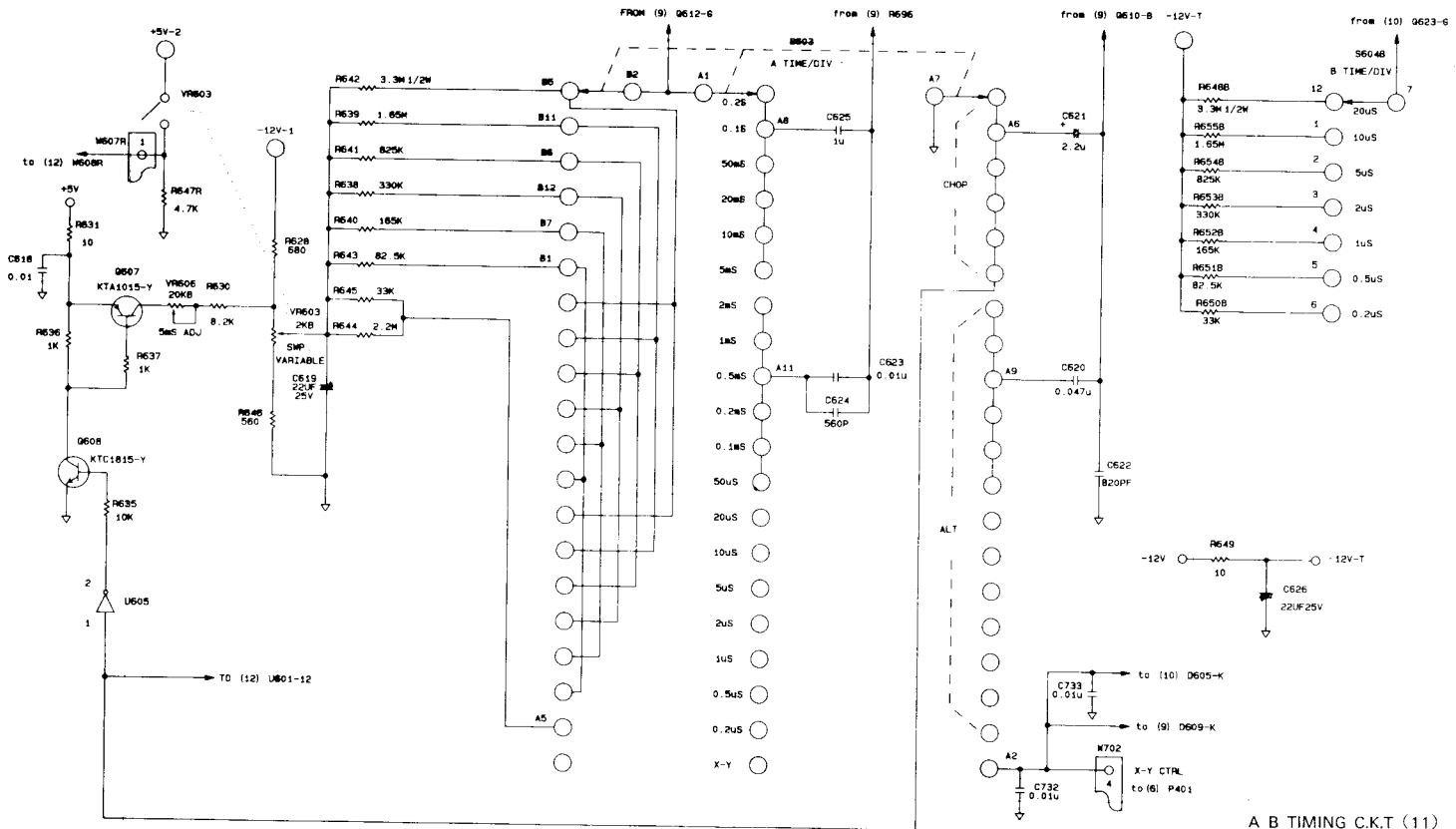


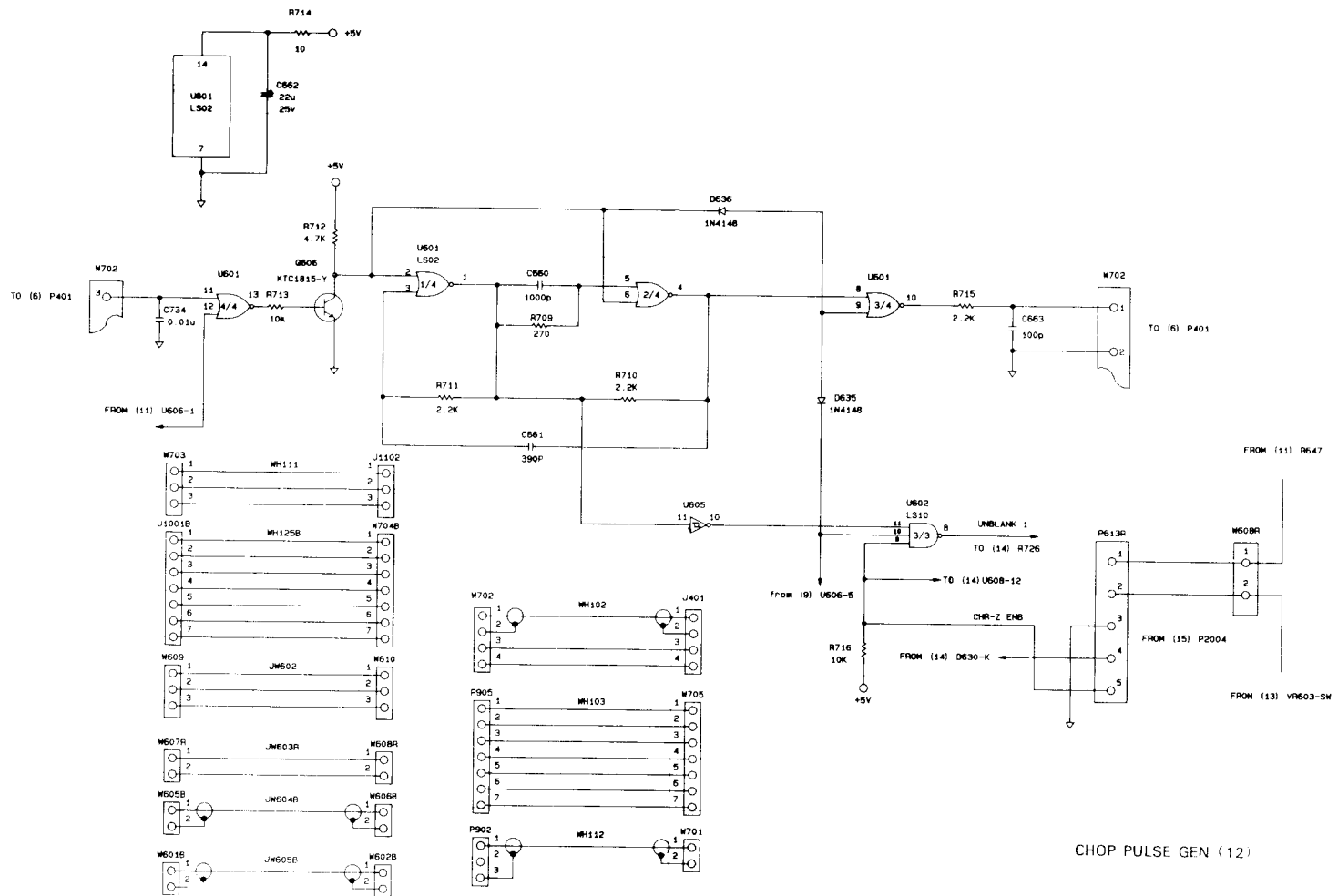


A SWEEP GEN (9)





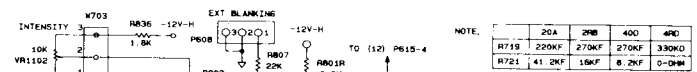
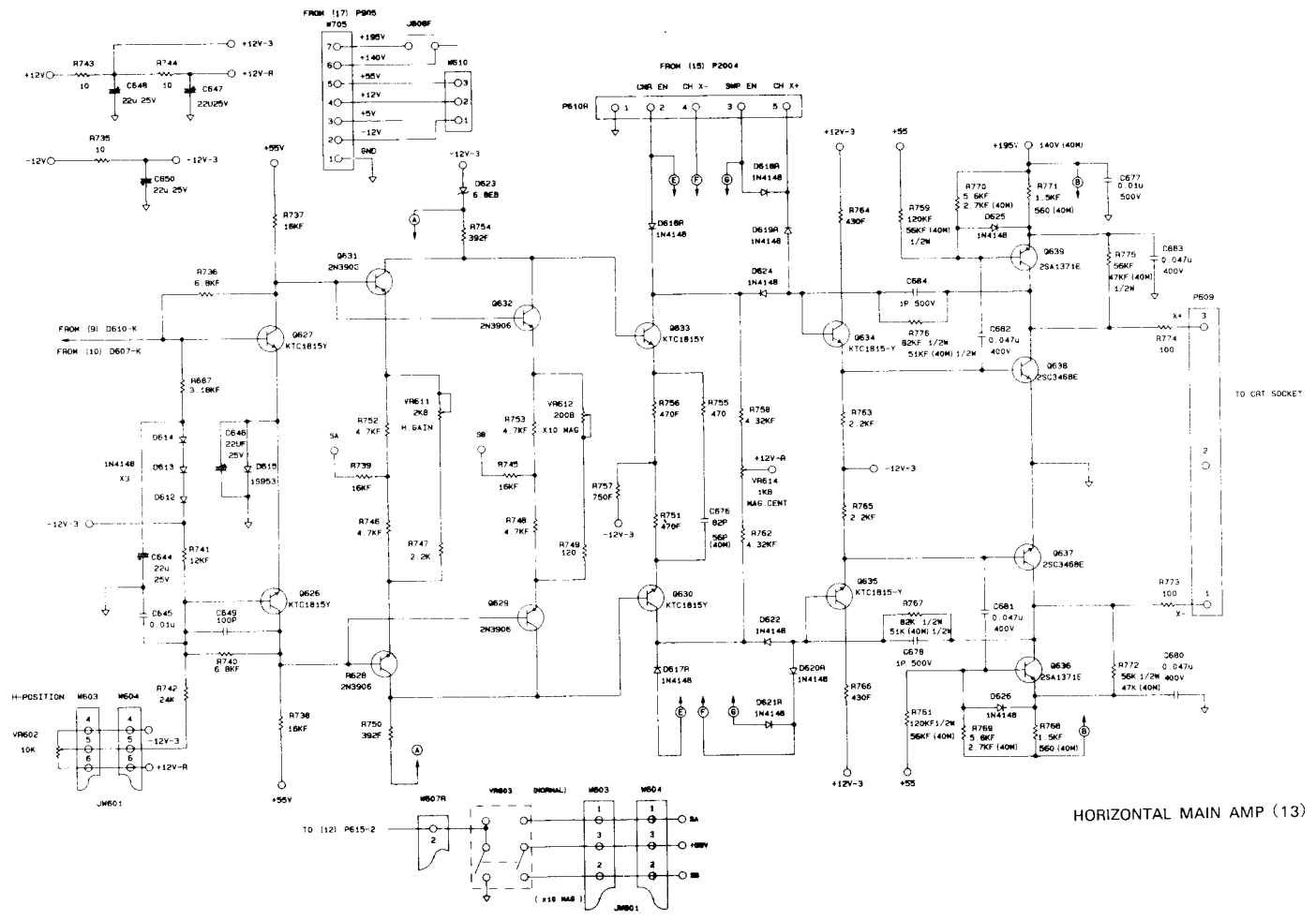




CHOP PULSE GEN (12)

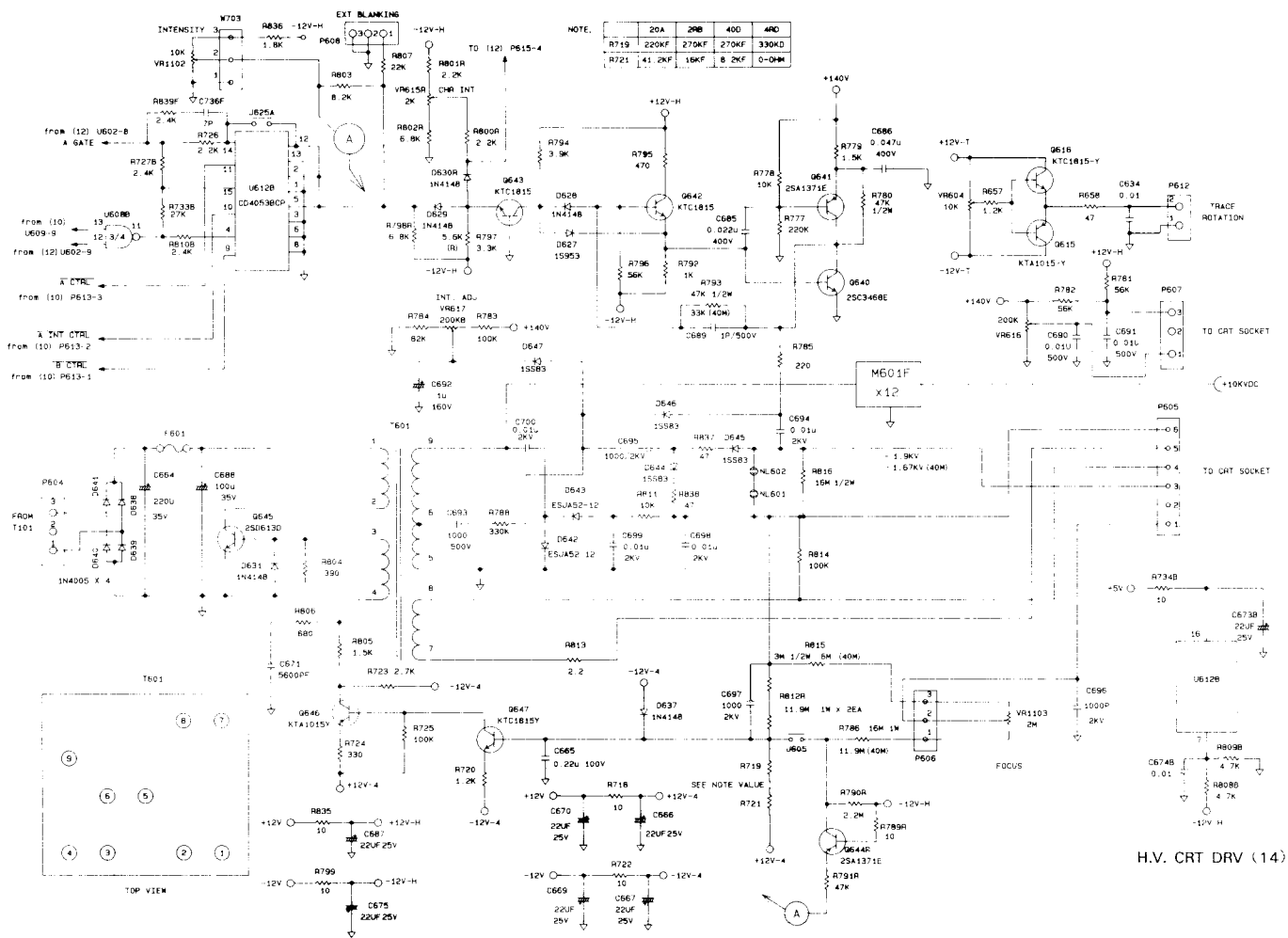
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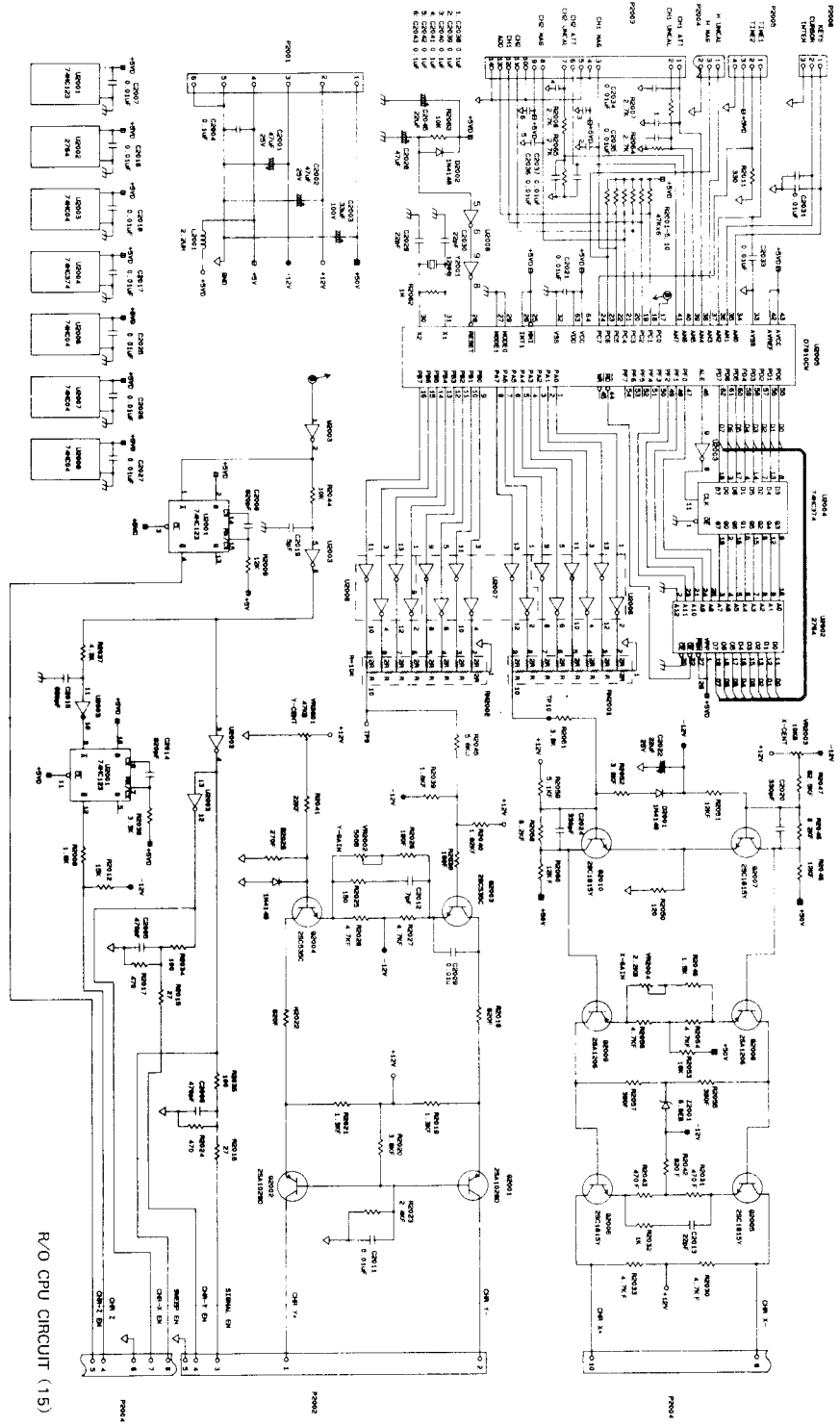


NOTE:

	20A	20B	40D	40D
R719	220K	270K	270K	330K
R721	41.2K	16K	8.2K	0-0H



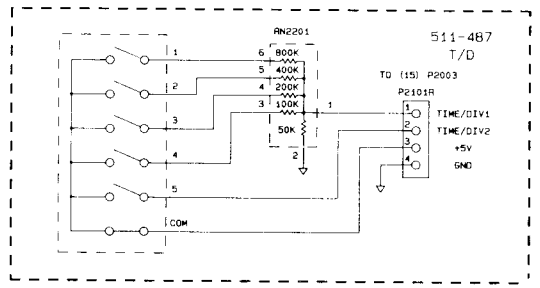
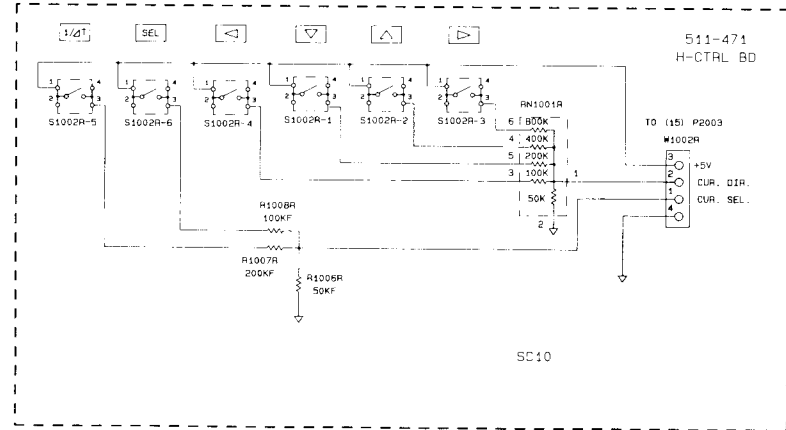
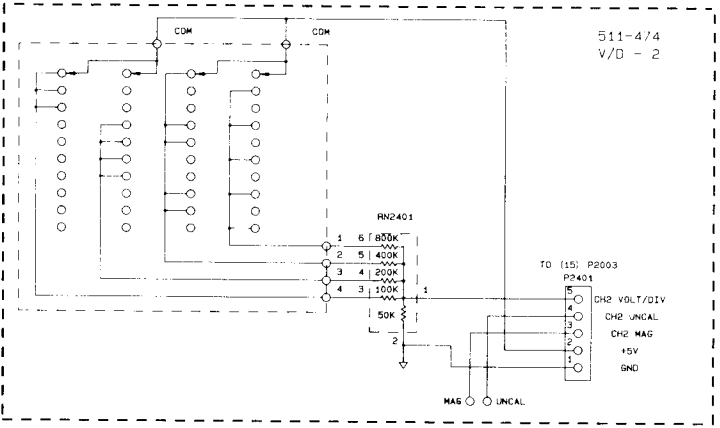
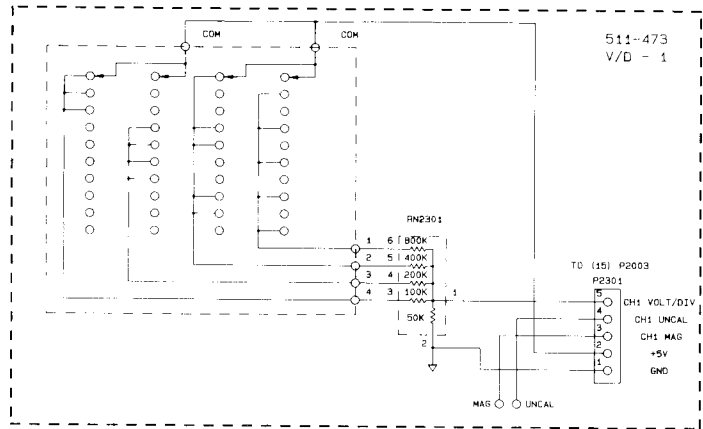
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NOTE:  $\phi$  DIGITAL GROUND  
 $\phi$  ANALOG GROUND

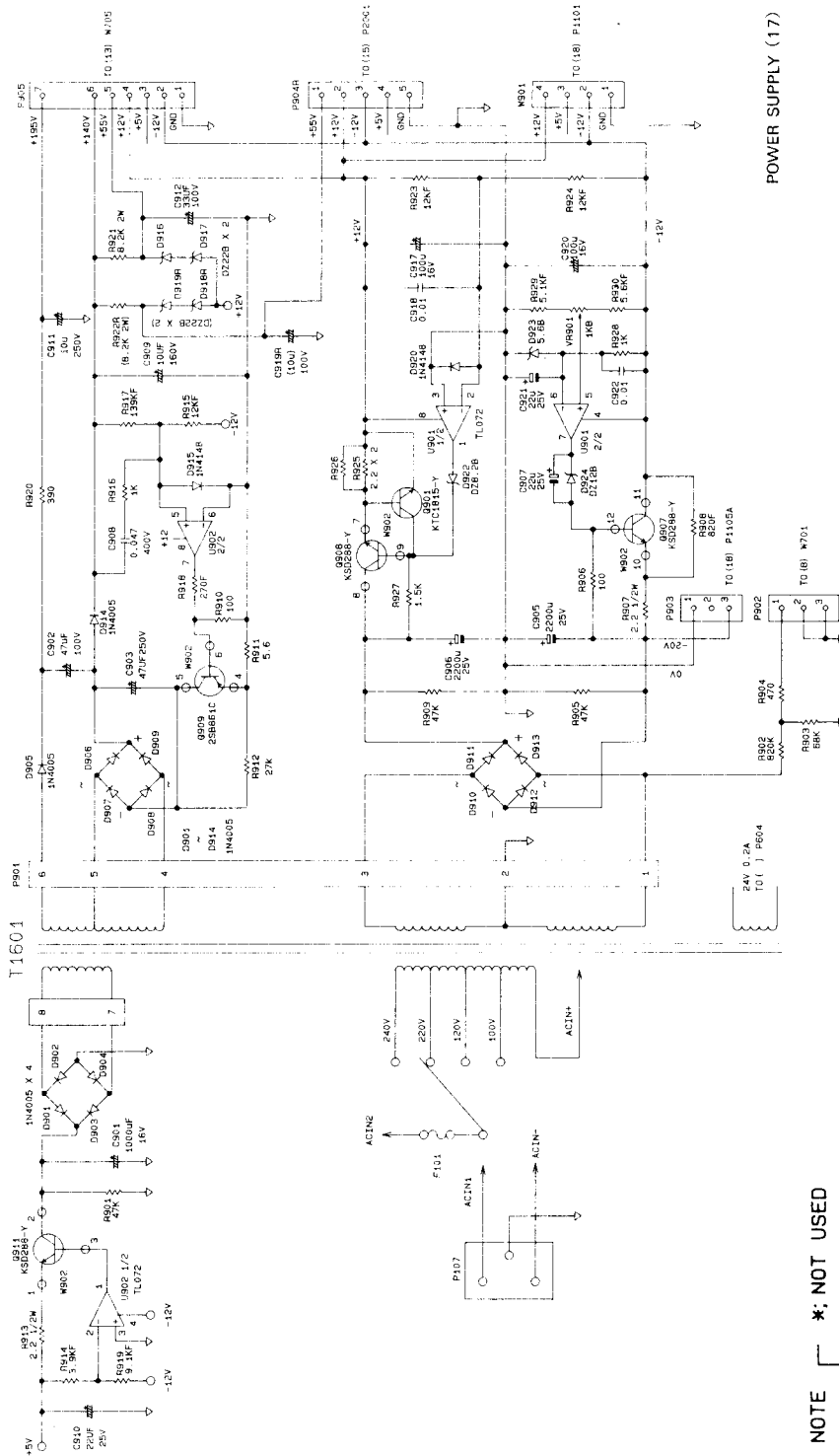
P2001: FROM (17) P2004  
 P2002: TO (6) P2004  
 P2003: FROM (6) P2004  
 FROM (6) P2004  
 FROM (6) P2004  
 P2004: TO (12) P2004  
 TO (13) P2004

R/O CPU CIRCUIT (15)

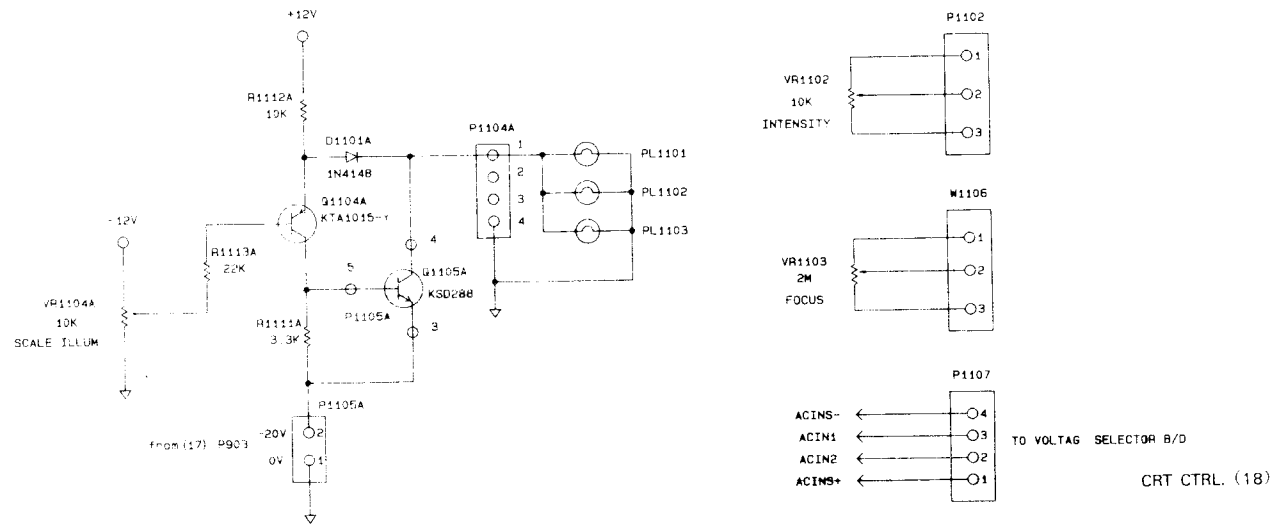
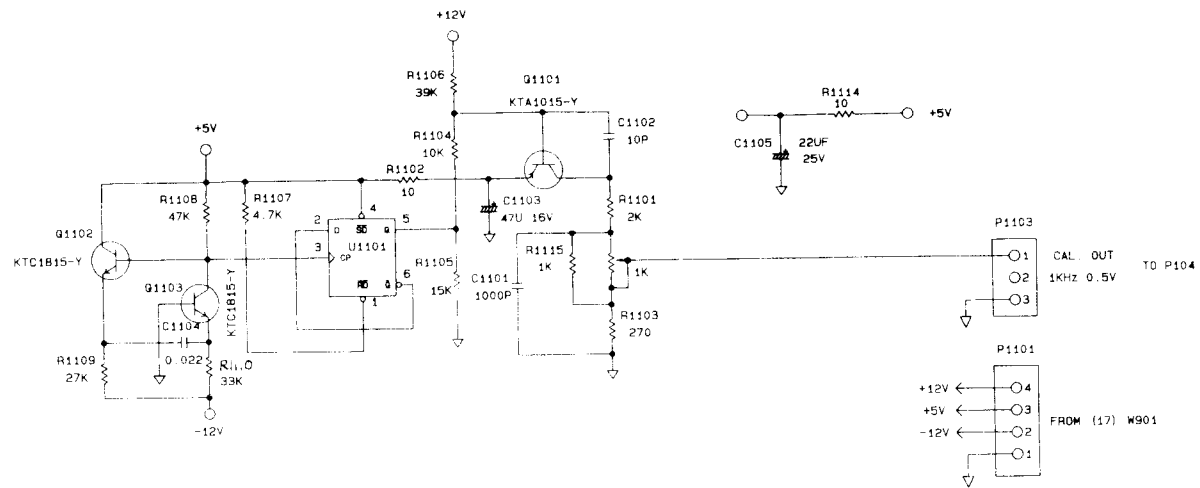


R/O SWITCH (16)

POWER SUPPLY (17)



NOTE \* : NOT USED  
 E <> : 40M ONLY  
 () : R/O ONLY



MEMO

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